Listing of Claims:

Claim 1 (Previously Presented): A liquid crystal display comprising:

a plurality of signal lines;

a plurality of scanning lines electrically connected to a scanning line control circuit; and

a plurality of pixels, each pixel comprising:

a liquid crystal cell having a pixel electrode and a storage capacitor, and

a switching transistor comprising a gate electrode connected to a scanning line, a drain electrode connected to one of the signal lines, and a source electrode connected to the pixel electrode, the gate electrode and the source electrode having an overlapping region, the size of the overlapping region of a pixel closer to the scanning line control circuit being smaller than the size of the overlapping region of another pixel farther from the scanning line control circuit.

Claim 2 (Previously Presented): The liquid crystal display of claim 1 wherein the gate electrode of each pixel comprises a first block located within the overlapping region, and an area of the first block of a pixel closer to the scanning line control circuit is smaller than an area of the first block of another pixel farther from the scanning line control circuit.

Claim 3 (Previously Presented): The liquid crystal display of claim 1 wherein the source electrode comprises a second block of each pixel located within the overlapping region, and an area of the second block of a pixel closer to the scanning line control circuit is smaller than an

area of the second block of another pixel farther from the scanning line control circuit.

Claim 4 (Original): The liquid crystal display of claim 2 wherein the gate electrode further comprises a pair of protective structures located on both sides of the first block for preventing the first block from being separated from the gate electrode.

Claim 5 (Previously Presented): A liquid crystal display comprising:

a scanning line connected to a scanning line control circuit;

a first region comprising at least a first transistor having a first gate electrode connected to the scanning line, a first drain electrode connected to a first signal line, and a first source electrode connected to a first pixel electrode, the first gate electrode and the first source electrode having a first overlapping region;

a second region located between the scanning line control circuit and the first region comprising at least a second transistor having a second gate electrode connected to the scanning line, a second drain electrode connected to a second signal line, and a second source electrode connected to a second pixel electrode, the second gate electrode and the second source electrode having a second overlapping region, the size of the first overlapping region being greater than the size of the second overlapping region; and

a third region located between the scanning line control circuit and the second region comprising at least a third transistor having a third gate electrode connected to the scanning line, a third drain electrode connected to a third signal line, and a third source electrode connected to a third pixel electrode, the third gate

electrode and the third source electrode having a third overlapping region, the size of the second overlapping region being greater than the size of the third overlapping region.

Claim 6 (Previously Presented): The liquid crystal display of claim 5 wherein the first gate electrode comprises a first block located within the first overlapping region, and the second gate electrode comprises a second block located within the second overlapping region, and an area of the first block is greater than that of the second block.

Claim 7 (Original): The liquid crystal display of claim 6 wherein the first gate electrode further comprises a pair of protective structures located on both sides of the first block for preventing the first block from being separated from the first gate electrode.

Claim 8 (Previously Presented): The liquid crystal display of claim 5 wherein the first source electrode comprises a third block located within the first overlapping region, and the second source electrode comprises a fourth block located within the second overlapping region, and an area of the third block is greater than an area of the fourth block.